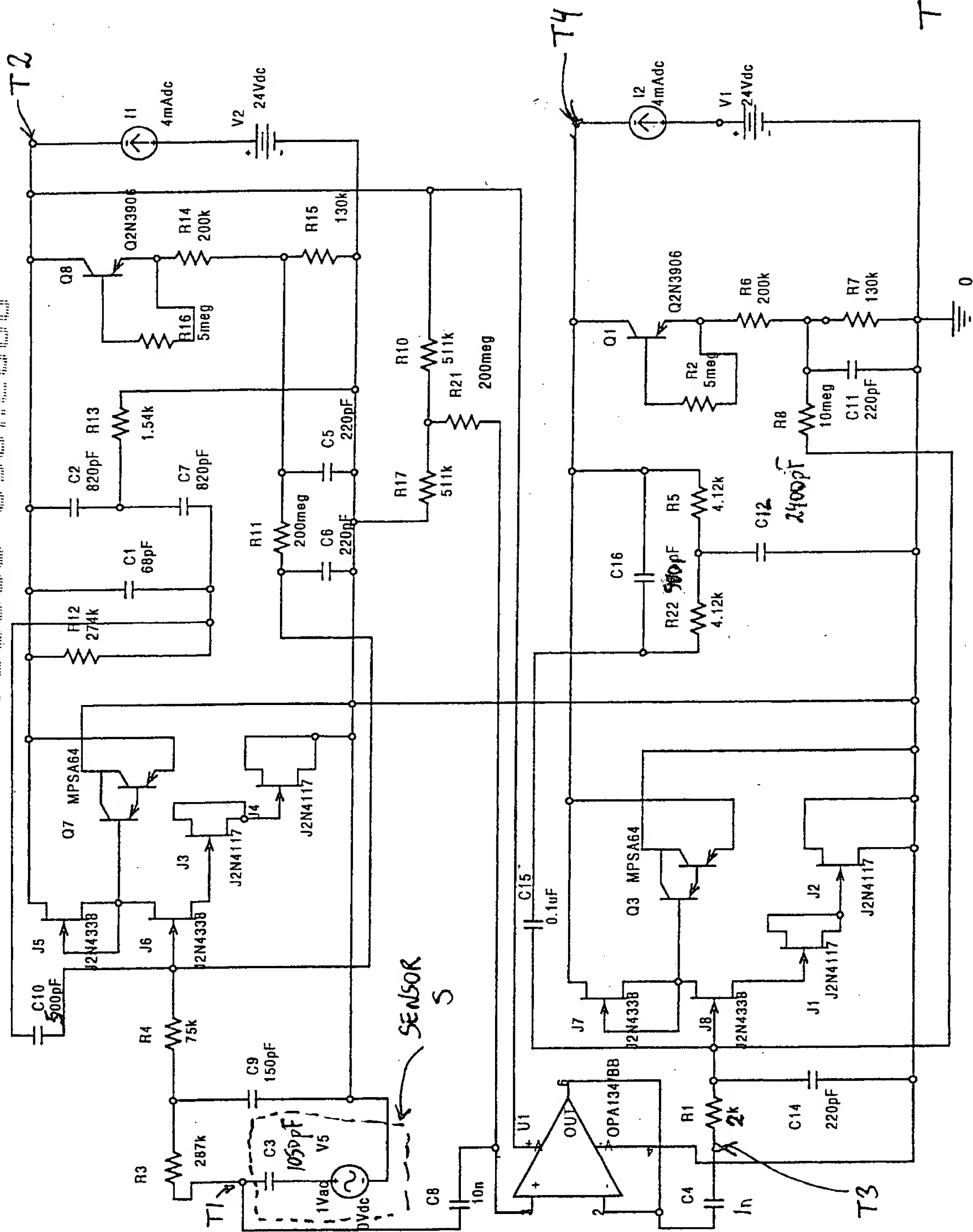


FIGURE 1

03030" 03642560



TRANSDUCER

13

Fig. 2

T0303070964660

$g=2$

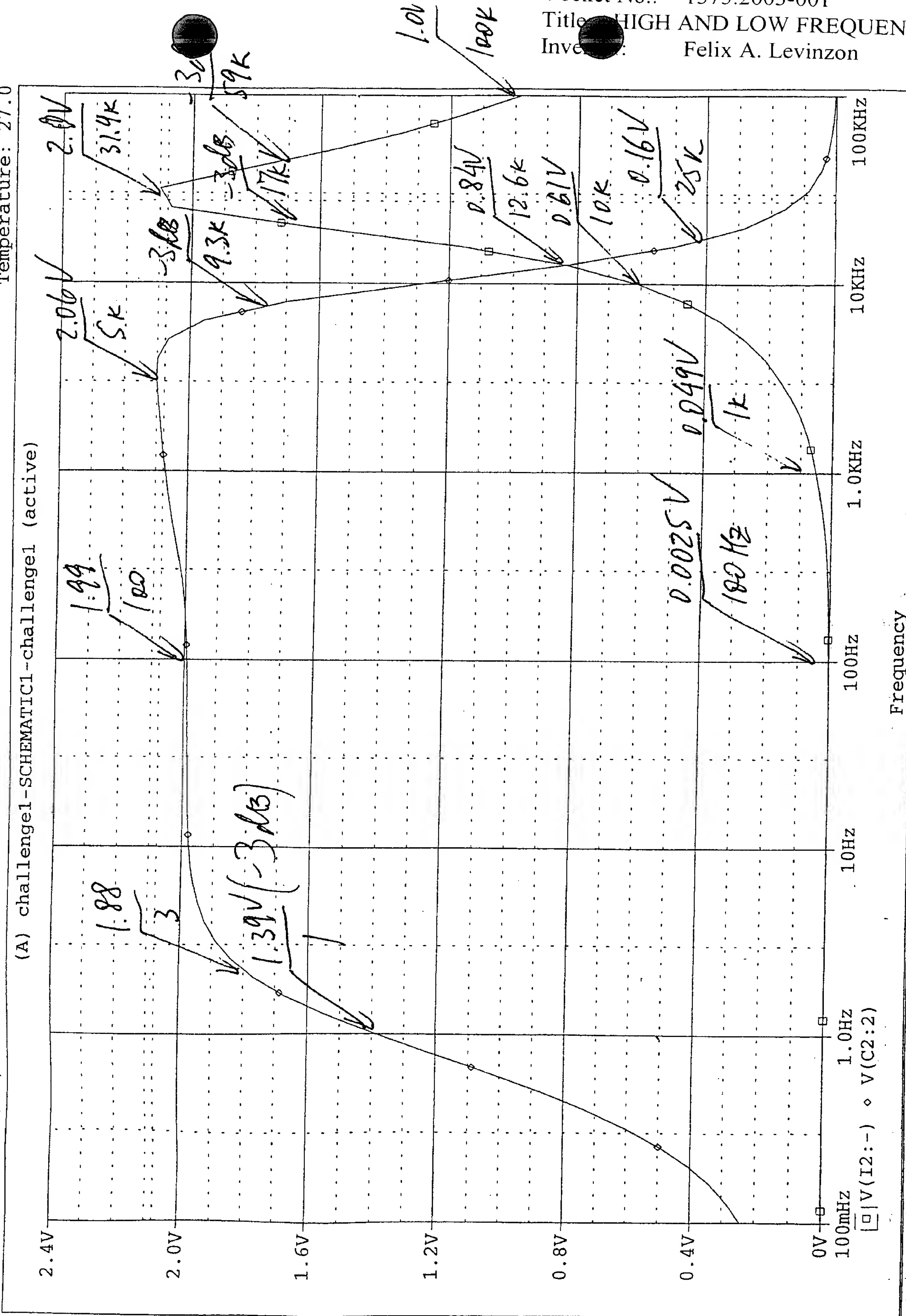
$V_{in} = 1V$

\*\* circuit file for profile: challenge1

Date/Time run: 05/02/00 13:45:58

Temperature: 27.0

(A) challenge1-SCHEMATIC1-challenge1 (active)



A1: (27.361K, 2.0764) A2: (100.000m, 745.615u) DIFF(A): (27.361K, 2.0756)

Date: May 02, 2000

Time: 13:46:18

Fig. 3

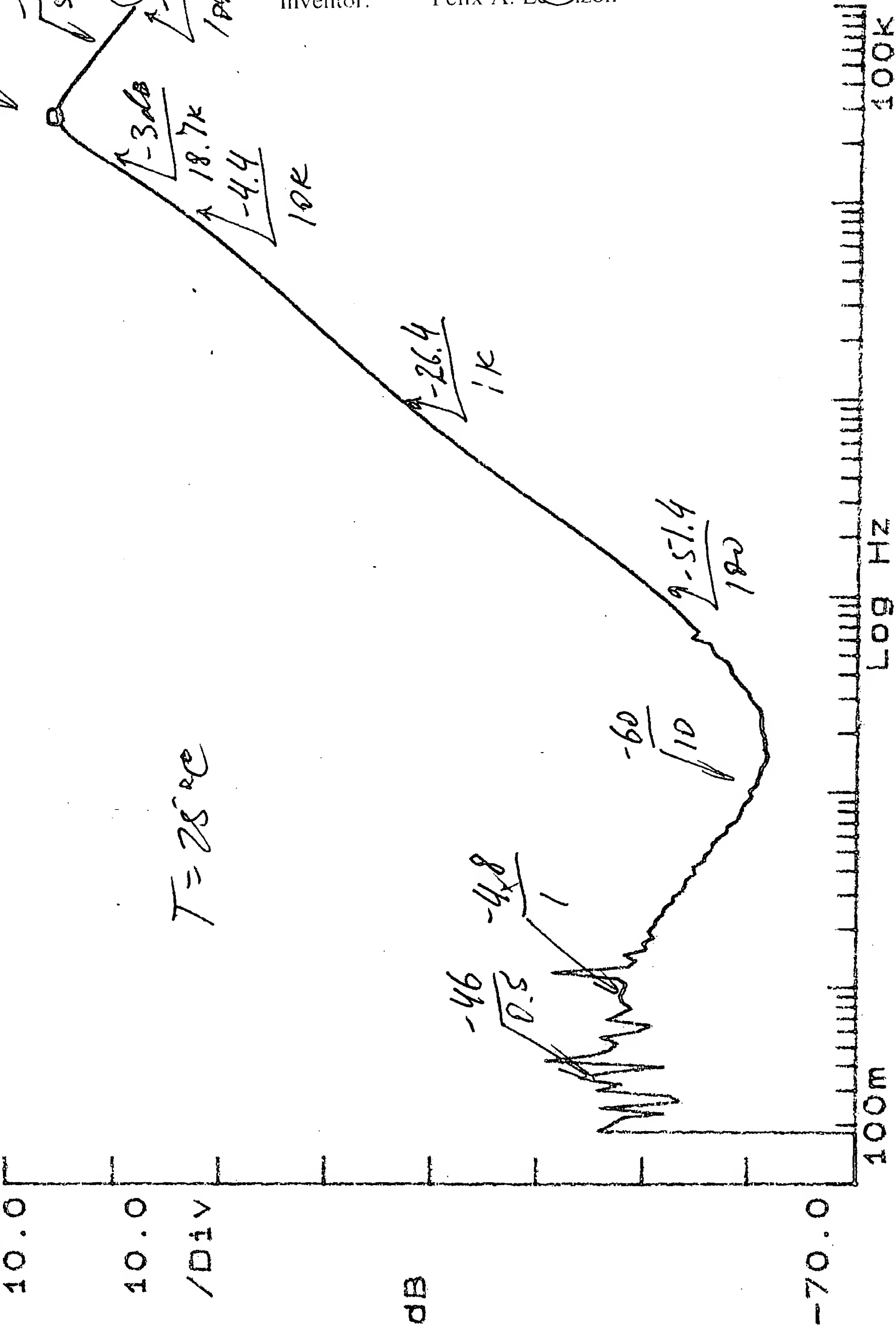
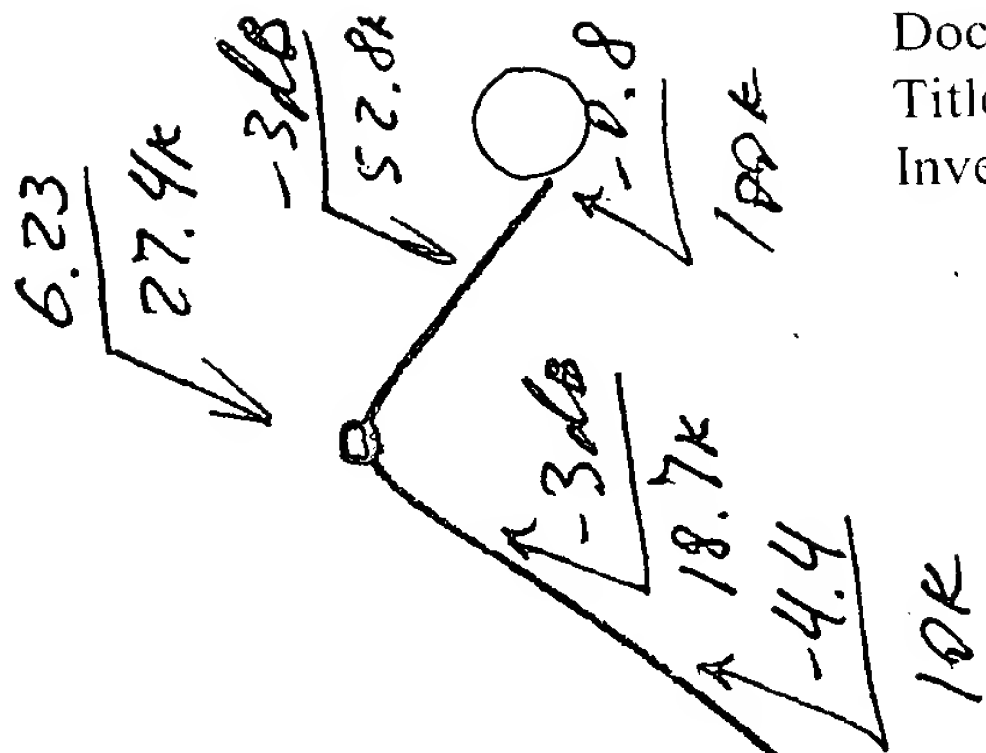
5/2/80

TE03030" 09642450 Channel

X=27.38KHZ  
Ya=6.22601 dB  
FREQ RESP  
10.0

$V_B = 13.4V$

$T = 28^{\circ}C$



Docket No.: 1575.2003-001  
Title: HIGH AND LOW FREQUENCY...  
Inventor: Felix A. Lizon

Fig. 4

5/2/00

VB = 11.1V

X=27.38KHZ  
 Ya=6.30532 dB  
 FREQ RESP  
 10.0

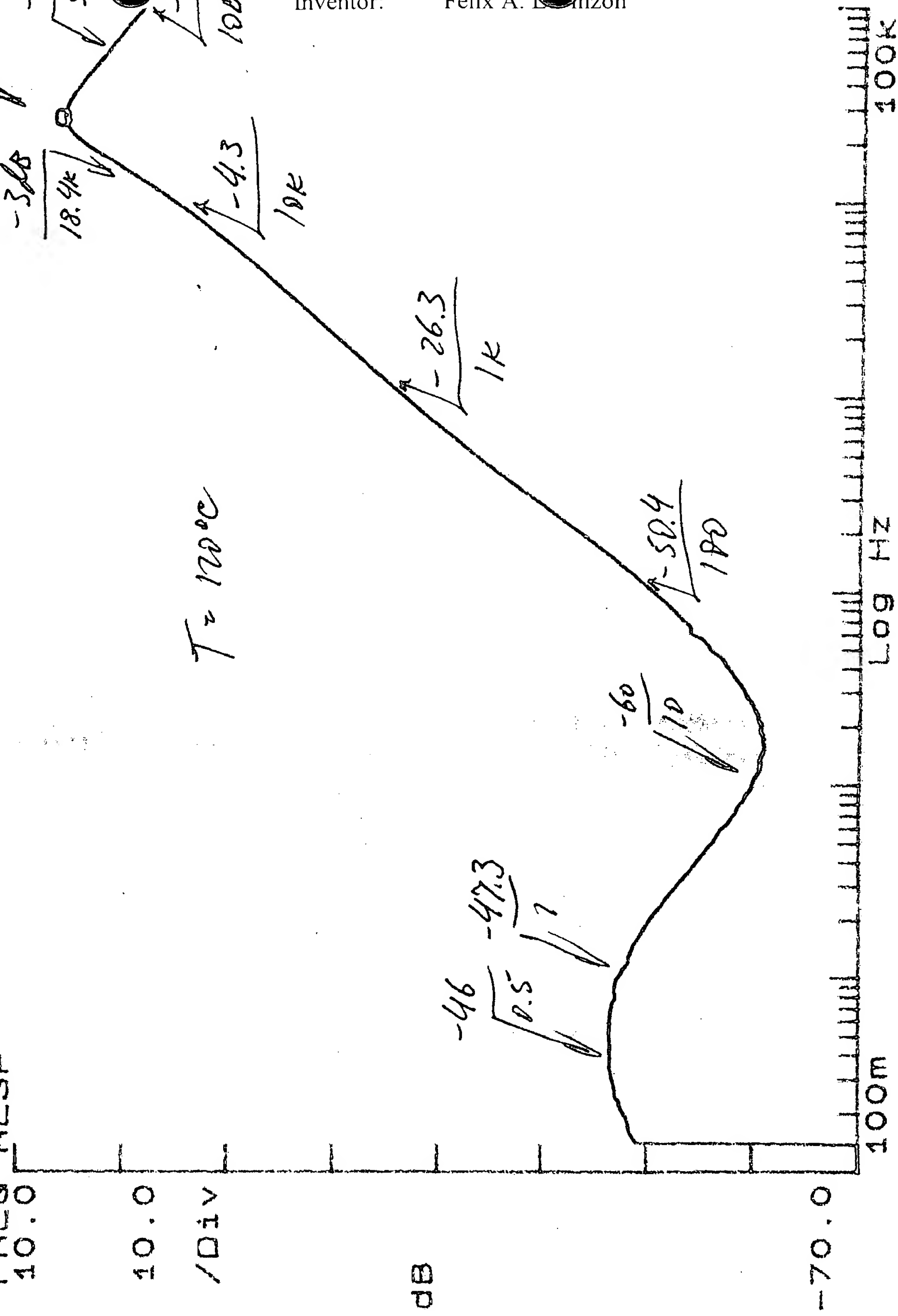


Fig. 5

5/2/80  
 LF Channel  
 $V_B = 12.7V$

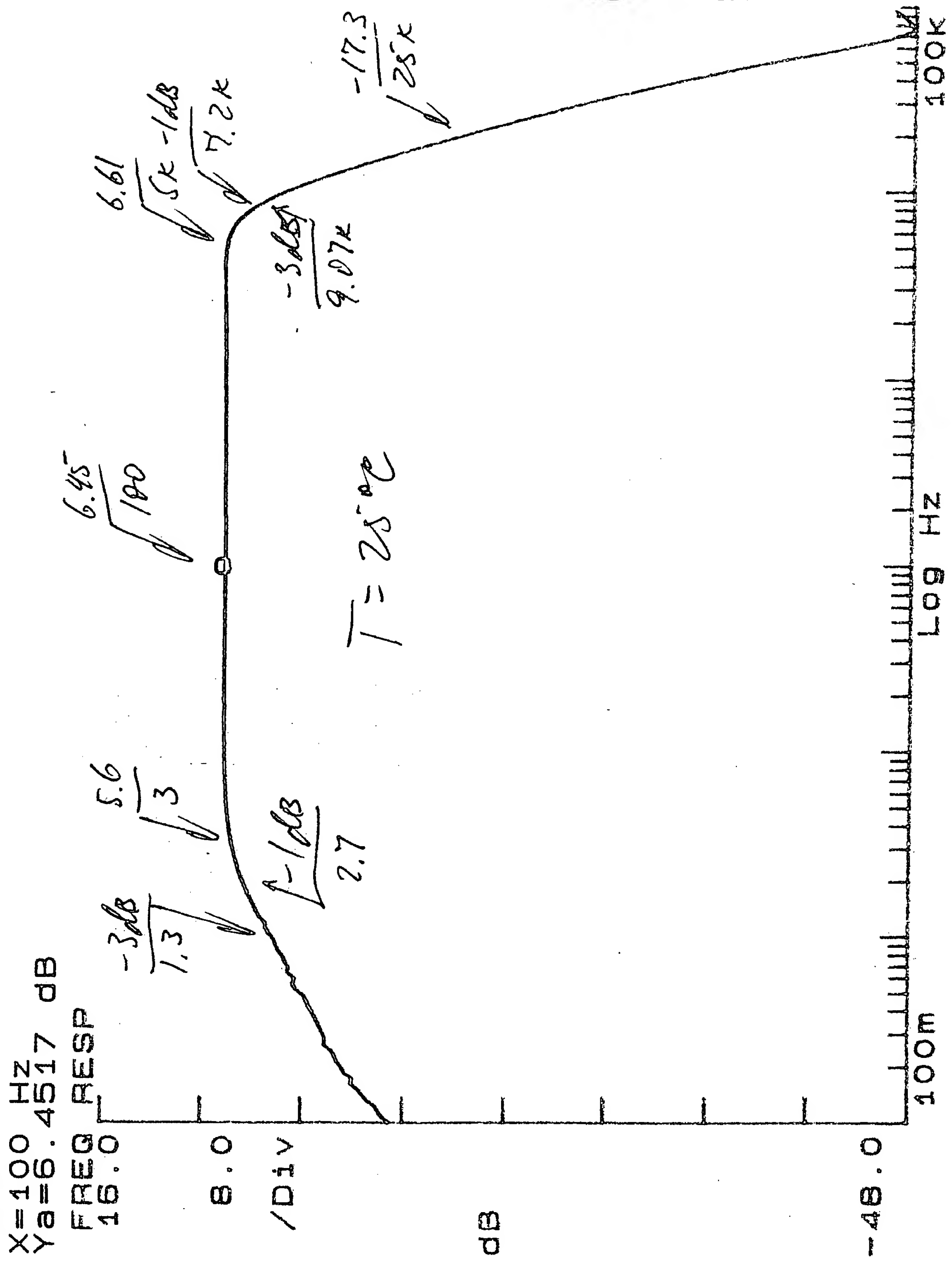


Fig. 6

TOP SECRET

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Inventor: Felix A. Levinzon

5/2/80

LF Channel

$V_B = 10.7V$

X=100 HZ  
Y=6.4987 dB

FREQ RESP  
16.0

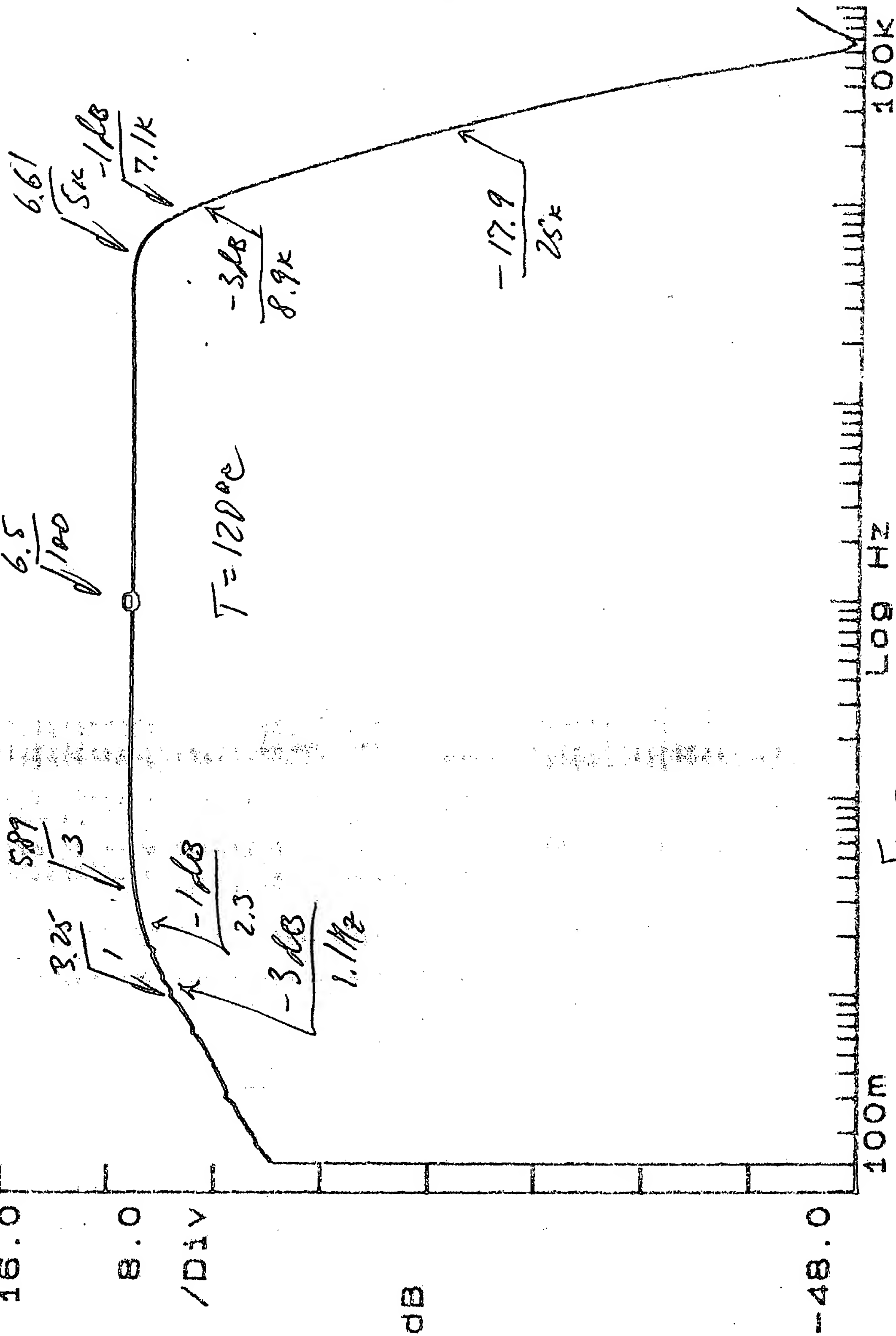


Fig. 7

5/3/80

LF Channel  $V_n / 1-30k\Omega = 25 \mu V$  rms  
 $V_n / 1-10k\Omega = 21 \mu V$  rms

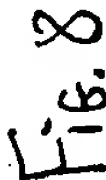
X=1.029 Hz  
Ya=2.83001  $\mu$ V/Hz

$$T = 250^\circ\text{C}$$

POWERHOUSE

3AV9 0%0V10

L.F.  $\rightarrow$  V.S. - Raffle  
H.F.  $\rightarrow$  Line, 24V, 4000' <sup>14</sup> 4000'



7001



108080" 09542650

5/3/80

$$V_n(1-30K) = 15 \mu V rms$$
$$V_n(1-100K) = 22 \mu V rms$$

HF Channel

L.F. - Battery 24V, 4mA  
H.F. - Line, 24V, 4mA

X=1.059 HZ  
Y=2.69781  $\mu V/\sqrt{HZ}$

POWER SPEC1

3AVG 0%OVIP

QV1

800  
/Div

$$T = 28 \mu s$$

$$\frac{2.7}{1}$$

Mag

rms  
 $V/\sqrt{HZ}$

$$\frac{0.4}{10}$$

$$\frac{0.1}{100}$$

$$\frac{0.04}{1K}$$

$$\frac{0.05}{10K}$$

$$\frac{0.11}{30K}$$

0.0

1

Log HZ

100K

Fig. 9

Docket No.: 1575.2003-001

Title: HIGH AND LOW FREQUENCY

Inventor: Felix A. Levinzon